ABSTRACT OF THE DISCLOSURE

FAST PAGING OF A LARGE MEMORY BLOCK

5   A method, system, and program for fast paging of a large memory block are provided. A request to physically remove a memory block device from a data processing system is received. A logical volume within disk space is dynamically allocated for a contiguous paging space. Multiple logical pages for the memory block devices are translated into multiple physical addresses for the memory block device. A single input/output request is issued to page out data

10   located at the multiple physical addresses to the contiguous paging space, such that after the single request is complete, the memory block device can be safely removed. Further, when a replacement memory block device is detected, a single input/output request is issued to page in data located within the contiguous paging space to the replacement memory block.